

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Application No.:	10/653,754	§	Examiner:	Dillon, Samuel A.
Filed:	September 3, 2003	§	Group/Art Unit:	2185
Inventor(s):	Stephan G. Meier, S. Craig Nelson, and Gene W. Shen	§	Atty. Dkt. No:	5500-97500
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Title:	Low Power Way-Predicted Cache	§		
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**RESPONSE TO OFFICE ACTION OF  
December 10, 2008**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of December 10, 2008, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.